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Cut design time with this unique all-region MOSFET modeling approach to analog circuit design. A unified treatment of noise and mismatch is presented, plus the basic building blocks of analog circuits. With many design examples and exercises, this is ideal for both students and circuit designers. --This text refers to the hardcover edition.

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290C Learning Goals Understand and use an all-region (accumulation, WI, MI and SI) compact MOSFET model for analog design Acquire a deep understanding (nonlinearities, noise, mismatch) of the basic CMOS build blocks and op amps Apply the above concepts in a design project 25 CMOS Analog Design Using All Region MOSFET Modeling

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LECTURE 23 DESIGN OF TWO-STAGE OP AMPS

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current I REF often high - not convenient for low-power design

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